

## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) GOGOI, BISHNU P. GROUP ART UNIT: 2823  
APPLN. NO.: 10/730230 EXAMINER: Michelle Estrada  
FILED: 12/8/2003  
TITLE: METHOD OF FORMING A SEAL FOR A SEMICONDUCTOR DEVICE

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**Certificate of Submission**

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

May 2, 2007  
Date of Submission

/Dora Hudgins/  
Signature

Dora Hudgins  
Printed Name of Person Signing Certificate

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated March 20, 2007, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.